



UNITED STATES PATENT AND TRADEMARK OFFICE  
APPLICANT Gowrishankar L. GROUP ART UNIT: 2829

Chindalore et al.

APPLN. NO.: 10/609,361

EXAMINER: Asok K. Sarkar

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CONFIRMATION No.: 8224

TITLE: NON-VOLATILE MEMORY HAVING A REFERENCE  
TRANSISTOR AND METHOD FOR FORMING

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AMENDMENT UNDER 37 CFR 1.312

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This paper is filed contemporaneous with the filing of the issue fee for this application. Response to the comments provided below is respectfully requested.

**FEES**

Applicants authorize the charging of Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any fees owed in connection with this application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any refunds.

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